

BAS16TT1G

Silicon Switching Diode

Features

- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Rating	Symbol	Max	Unit
Continuous Reverse Voltage	V_R	100	V
Recurrent Peak Forward Current	I_F	200	mA
Peak Forward Surge Current Pulse Width = 10 μs	$I_{FM(\text{surge})}$	500	mA

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) $T_A = 25^\circ\text{C}$ Derated above 25°C	P_D	225	mW
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	555	$^\circ\text{C}/\text{W}$
Total Device Dissipation, FR-4 Board (Note 2) $T_A = 25^\circ\text{C}$ Derated above 25°C	P_D	360	mW
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	345	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

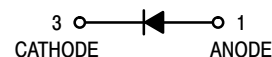
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-4 @ Minimum Pad
2. FR-4 @ 1.0 x 1.0 Inch Pad

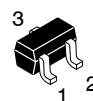


ON Semiconductor®

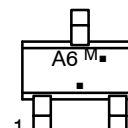
<http://onsemi.com>



MARKING DIAGRAM



CASE 463
SOT-416
STYLE 2



XX = Specific Device Code
M = Date Code
■ = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping†
BAS16TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Forward Voltage (I _F = 1.0 mA) (I _F = 10 mA) (I _F = 50 mA) (I _F = 150 mA)	V _F	–	715 866 1000 1250	mV
Reverse Current (V _R = 100 V) (V _R = 75 V, T _J = 150°C) (V _R = 25 V, T _J = 150°C)	I _R	–	1.0 50 30	μA
Capacitance (V _R = 0, f = 1.0 MHz)	C _D	–	2.0	pF
Reverse Recovery Time (I _F = I _R = 10 mA, R _L = 50 Ω) (Figure 1)	t _{rr}	–	6.0	ns
Stored Charge (I _F = 10 mA to V _R = 6.0 V, R _L = 500 Ω) (Figure 2)	Q _S	–	45	PC
Forward Recovery Voltage (I _F = 10 mA, t _r = 20 ns) (Figure 3)	V _{FR}	–	1.75	V

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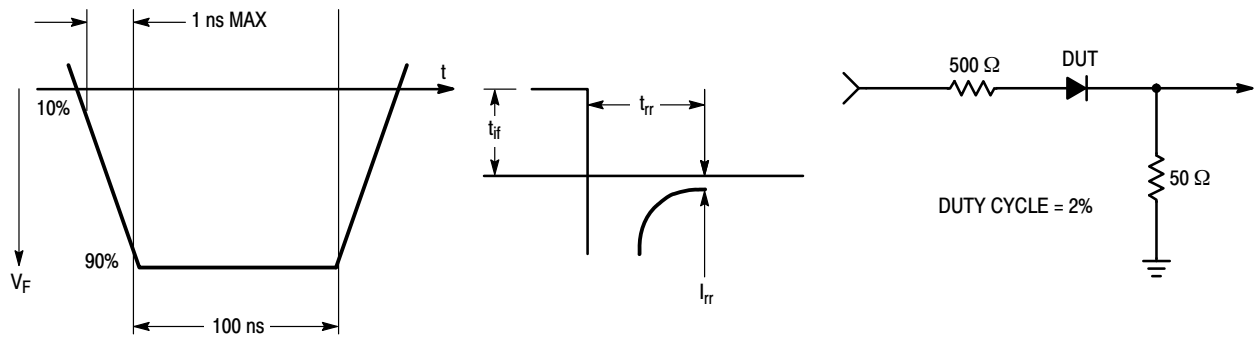


Figure 1. Reverse Recovery Time Equivalent Test Circuit

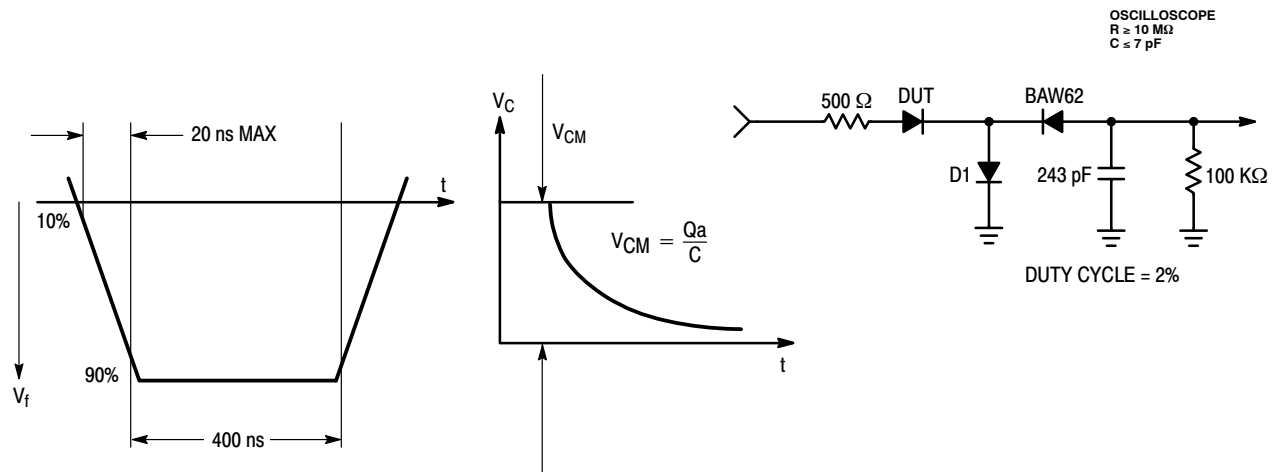


Figure 2. Stored Charge Equivalent Test Circuit

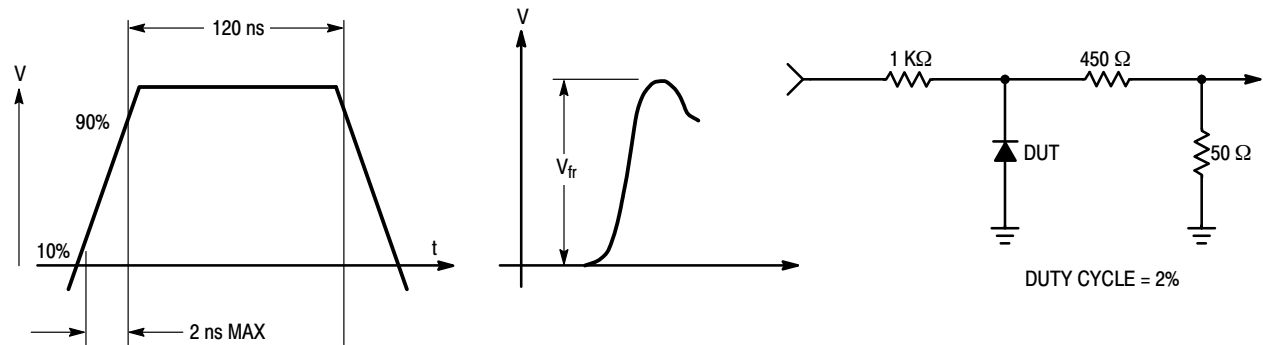


Figure 3. Forward Recovery Voltage Equivalent Test Circuit

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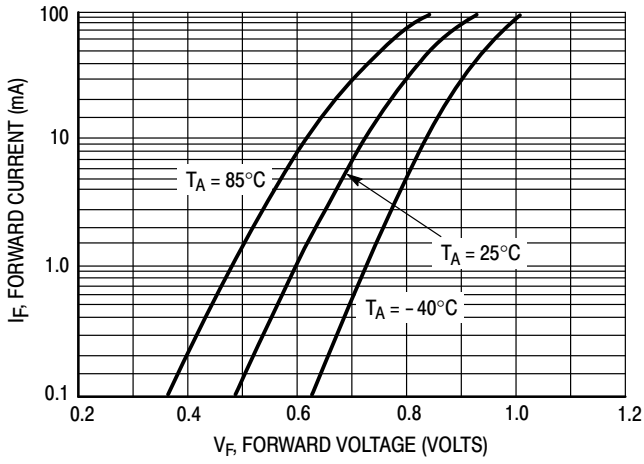


Figure 4. Forward Voltage

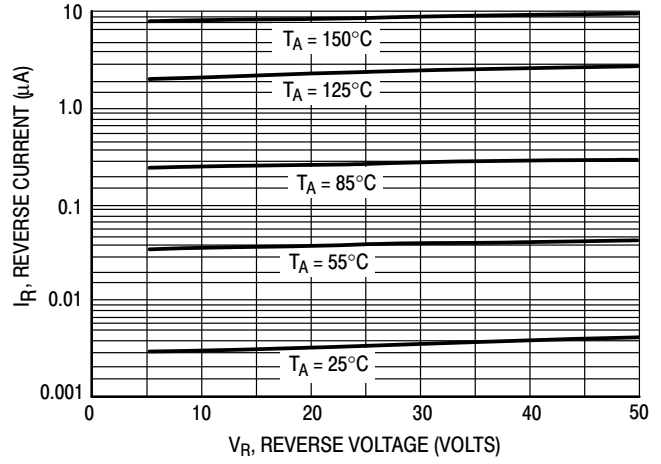


Figure 5. Leakage Current

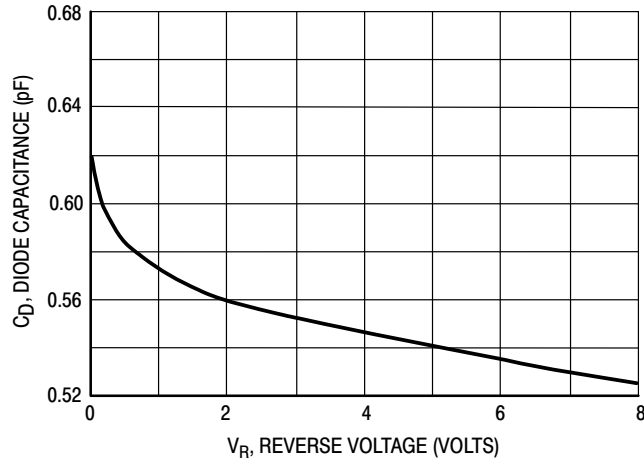


Figure 6. Capacitance

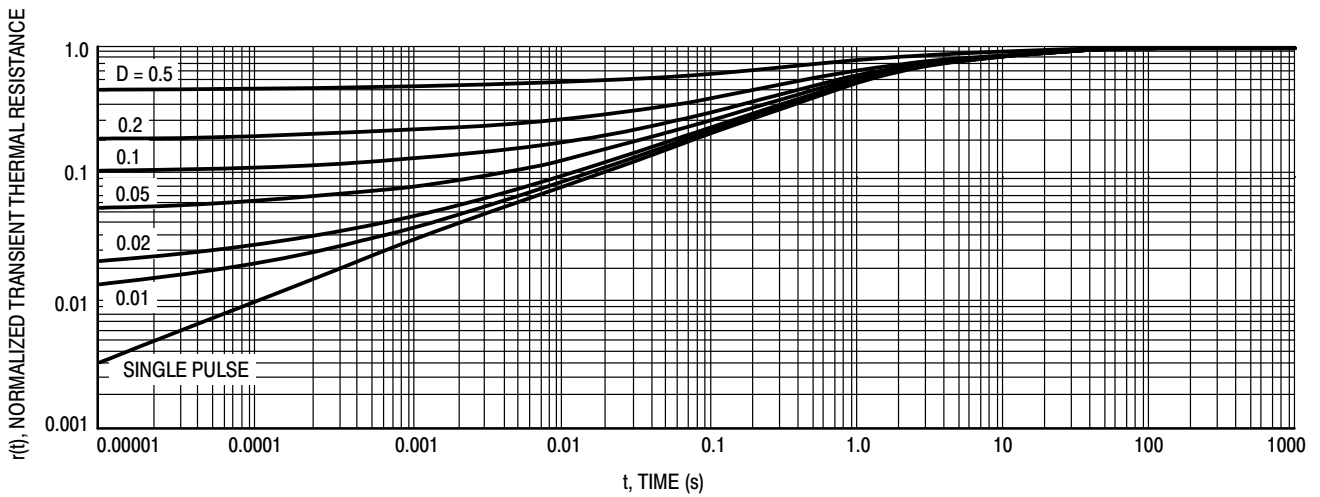
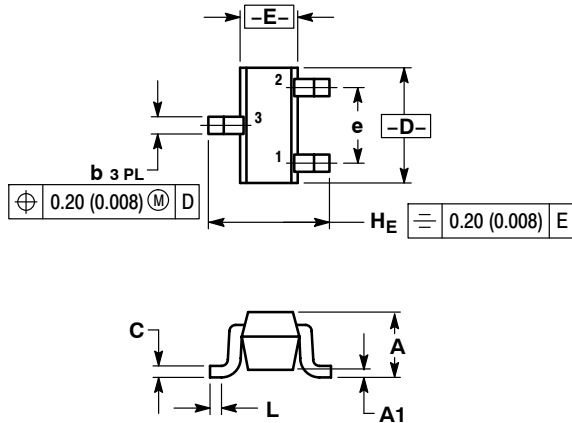


Figure 7. Normalized Thermal Response

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PACKAGE DIMENSIONS

SC-75/SOT-416
CASE 463-01
ISSUE F



NOTES:

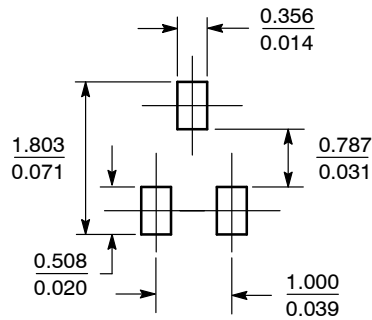
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
C	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.059	0.063	0.067
E	0.70	0.80	0.90	0.027	0.031	0.035
e	1.00 BSC			0.04 BSC		
L	0.10	0.15	0.20	0.004	0.006	0.008
H _E	1.50	1.60	1.70	0.061	0.063	0.065

STYLE 2:

1. ANODE
2. N/C
3. CATHODE

SOLDERING FOOTPRINT*



SCALE 10:1 ($\frac{\text{mm}}{\text{inches}}$)

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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